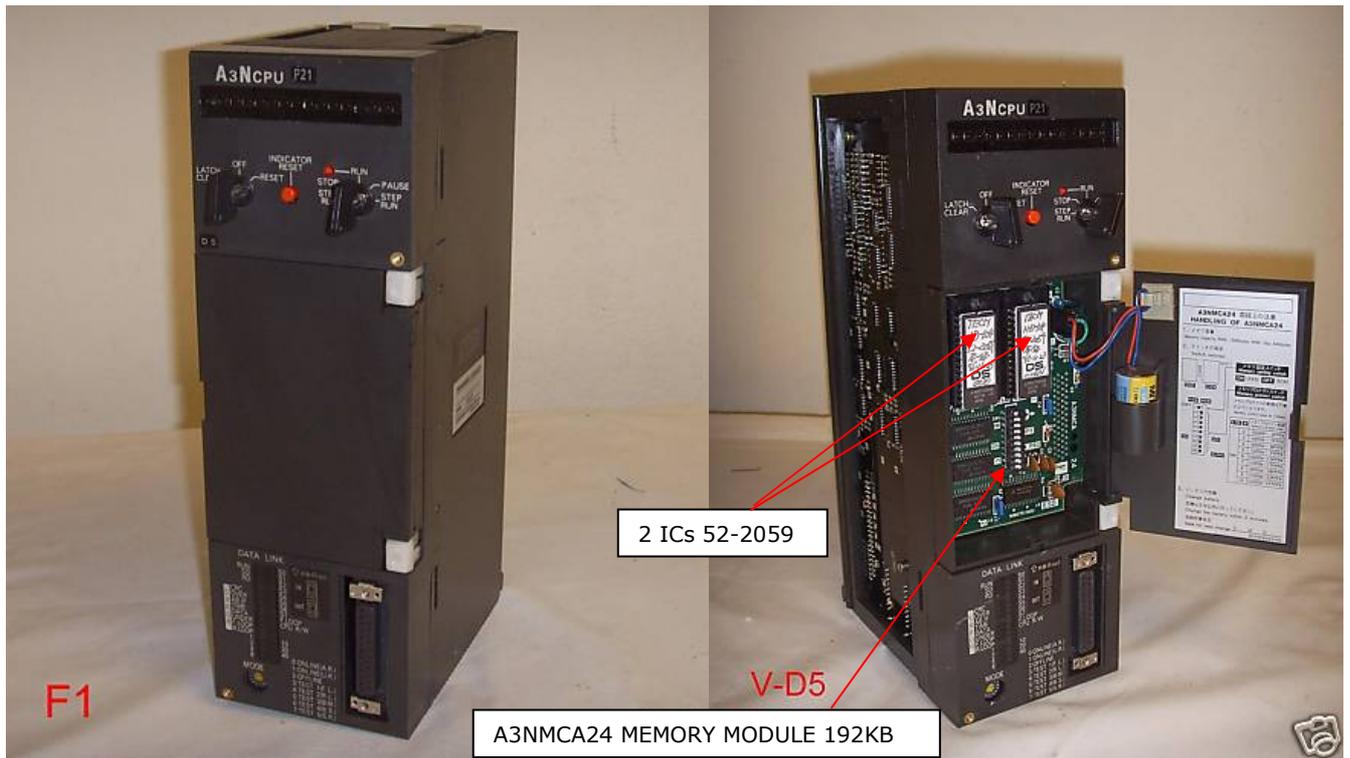


NETWORK CPU 2048PTS FIBER LINK

Mitsubishi melsec A3NCPUP21 (A3N-CPU-P21)
With A3NMCA24 Memory Module 192K mounted with 2 ICs 52-2059



Overview

Count: 2048 count input and output device points: 2048 points Program capacity: 30k steps (main), 30k steps (sub) basic instruction processing speed (LD instruction): 1.0 μ s optical data link capabilities

Features

Input switching control scheme

The following three input-output control system is set to change as possible. Input: direct method, output: direct method, Input: I / O image refresh method, output: direct method, Input: I / O image refresh method, output: I / O image refresh method

Constant scan function

Konsutansukyan feature, set the start of the scan cycle sequencing program is the ability to run the program in a certain time interval. Typically, the program scan time, or instructions to be executed by different criteria, or interrupt the program depends on whether the scan time for each scan subroutine program execution. If you use a constant scan function can be performed while maintaining a constant distance to control external devices by scanning the time variation of the program

Clock function

CPU clock has a built-in elements: year, month, day, hour, minute, second, read the day, you can write. CPU and equipped with the front LED indicators, a special relay ON by the M9027, LED indicator month, day, hour, minute, seconds can be displayed.

Desorption of input and output units online at

Usually, the power ON and output during the desorption unit, input signal lines and power lines / may break by the timing of the cutting elements within the unit. In this system, the power ON is designed to allow input into the desorption unit. Also, the PLC CPU is processing the case of desorption of the special register (D9094), special relays (M9094) by setting the data to the PLC CPU to perform the desorption unit of output without having to stop operations can.

ACPU program compatibility

ACPU program created in sequence can be used to AnNCPU. ACPUs sequence program, except the following instruction is a special relay can be used directly AnNCPU. (CHK instruction, SEG instruction, EI instruction, DI instruction, PR instruction, CHG instruction: Use different.) M9050: AnNCPU does not use.

Speed instruction

Instruction processing time is AnNCPU, ACPUs compared to 10 to 20 percent faster

GI cable-ready products

A3NCPUP21 the GI of a cable-ready lineup A3NCPUP21-S3

Performance Specifications

Item		Specifications
Control scheme		Repeated operations (stored program)
Output control system		Refresh method / direct method can be selected
Programming language	Sequence control dedicated language	Relay symbol language, language Rojikkushinborikku, MELSAP-□ (SFC)
Of instruction (types)		Instruction sequence: 26, basic instructions: 132 instructions Application: 110
Processing speed (instruction sequence)		Direct time: 1.0 ~ 2.3μs / step, refresh: 1.0μs / step
Memory		Minute amount of memory installed (up to 320k bytes)
Program capacity	Main sequence	Step up to 30k
	Subsequence	Step up to 30k
Count input		2048 points (X/Y0 ~ 7FF)
Device count	Internal relay [M]	1000 points (M0 ~ 999) M, S, L shared a total of 2048 (set by parameter)
	Latching relays [L]	1048 points (L1000 ~ 2047) M, S, L shared a total of 2048 (set by parameter)
	Step Relay [S]	0 points (no initial) M, S, L shared a total of 2048 (set by parameter)
	Link Relay [B]	1024 points (B0 ~ 3FF)
	Timer [T]	256 points 100ms timer: set time 0.1 ~ 3276.7s (T0 ~ 199), 10ms timer: set time 0.01 ~ 327.67s (T200 ~ 255), 100ms timer counter: set time 0.1 ~ 3276.7s (set by parameter)
	Counter [C]	Usually 256 points counter: Setting Range 1 ~ 32767 (C0 ~ 255), program counter for interrupt: set the range 1 to 32767 (set by parameter)
	Data register [D]	1024 points (D0 ~ 1023)
	Link register [W]	1024 points (W0 ~ 3FF)
	Anansheta [F]	256 points (F0 ~ 255)
	File register [R]	Up to 8192 points (R0 ~ 8191)
	Accumulator [A]	2 points (A0, A1)
	Index register [V, Z]	2 points (V, Z)
	Pointer [P]	256 points (P0 ~ 255)
	Interrupt pointer [I]	32 points (I0 ~ 31)
Special relay [M]	256 points (M9000 ~ 9255)	
Special register [D]	256 points (D9000 ~ 9255)	
Number of comments (64 per point)		Up to 4032 points
STOP → RUN time output mode switching		STOP re-state the output of the previous operation / output selection operation after execution
Self diagnostics		Traffic monitoring operations, anomaly detection memory, CPU error detection, output fault detection and anomaly detection cell
Operation mode error		Stop / select continue
Uotchidogutaima (WDT)		10 ~ 2000ms
RUN at the start of the scheme		Start Initial (power / CPU when power is restored after a power outage in the "RUN" switch ON automatically restart)
Latch (hold power outage) range		L1000 ~ L2047 (default) (L, B, T, C, D, W latch range available for setting)
Remote RUN / PAUSE contacts		X0 ~ X7FF from RUN / PAUSE accepted one set for each contact point, PAUSE contacts not only set
Art Title Registration		Yes (128 characters)
Register keyword		In
Output assignment		0 to 64 points (16 per point)
Clock function		Year, month, day, hour, minute, seconds, day (Auto leap year) Accuracy: -3.9 ~ +0.8 s (TYP.-1.1s) / d at 0 ° C, -1.8 ~ +1.0 s (TYP. -- 0.2s) / d at 25 ° C, -8.5 ~ -0.7s (TYP.-4.0s) / d at 55 ° C

Allowable Voltage	By the power supply
Current consumption	1.55A
Dimensions	254 (H) × 79.5 (W) × 121 (D) mm
Mass	0.95kg